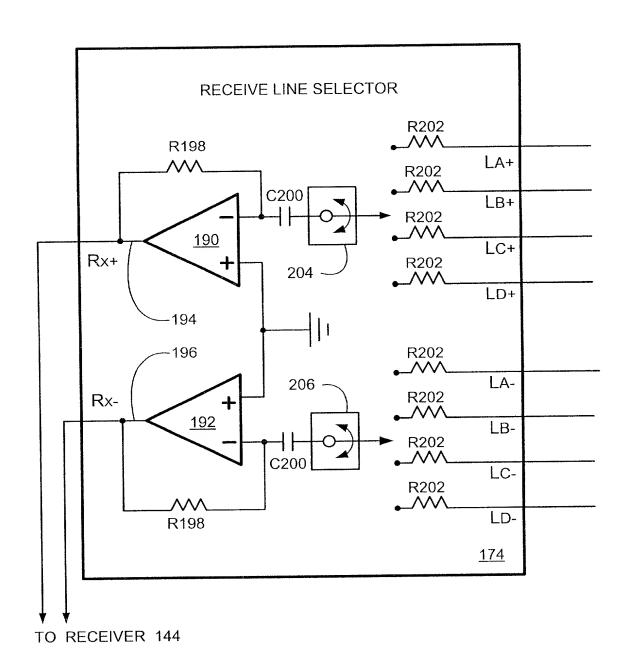
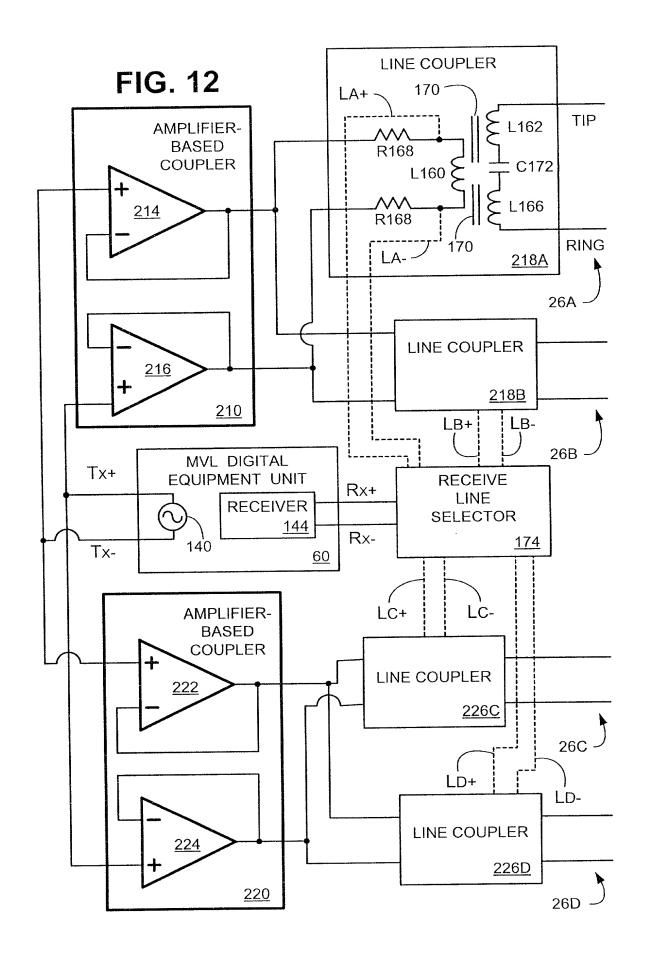
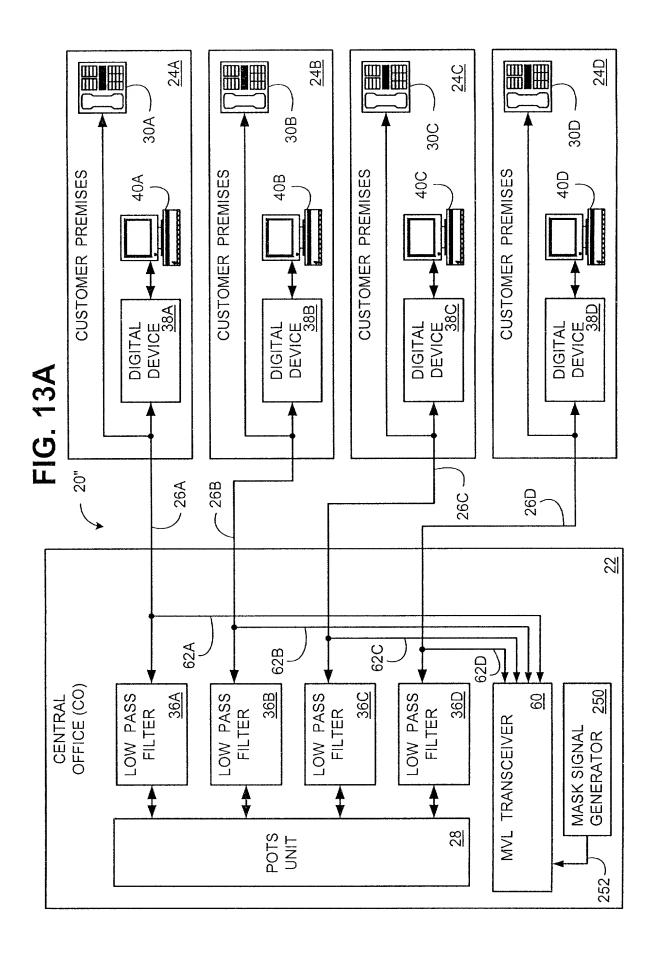
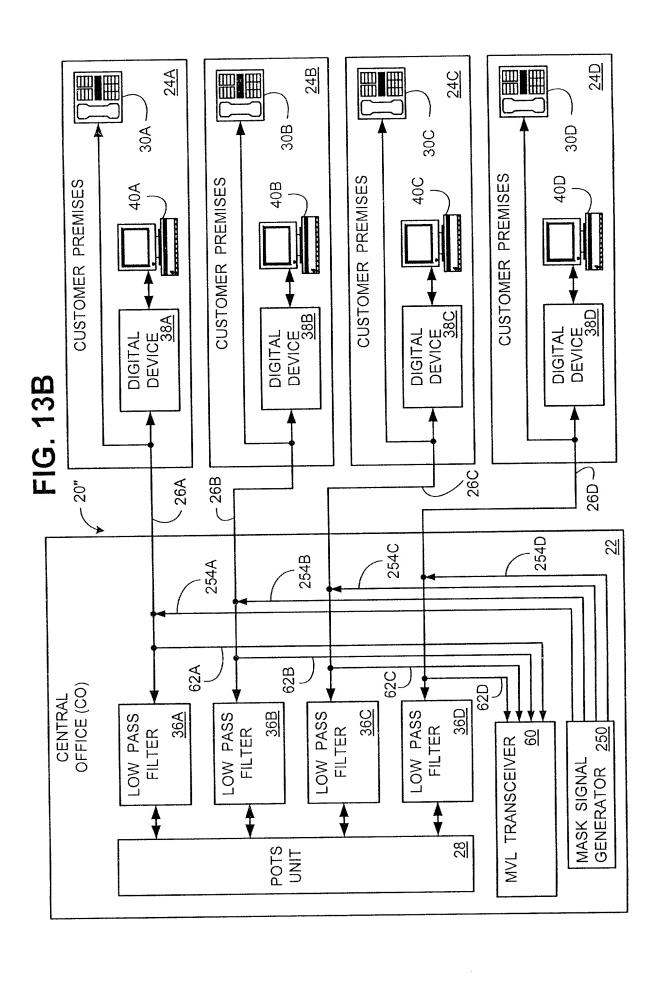


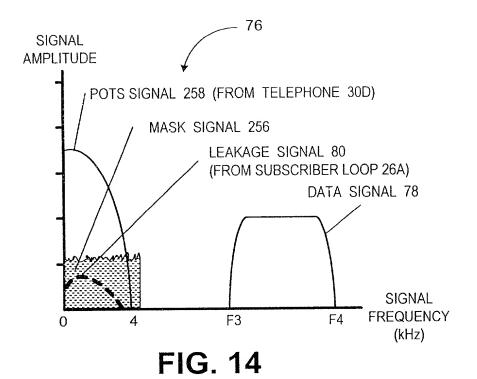
FIG. 11











MASK SIGNAL GENERATOR 267 -**DETECTOR** 278 -**INTERFACE** 277 <u>264</u> TO USER 261 -**INTERFACE** 276 **DEVICE** 268 266 **PROCESSOR** 252 <u> 262</u> 260 272 261-252 **MEMORY** FIG. 15 **LOGIC** <u> 269</u> <u>270</u> <u>250</u>

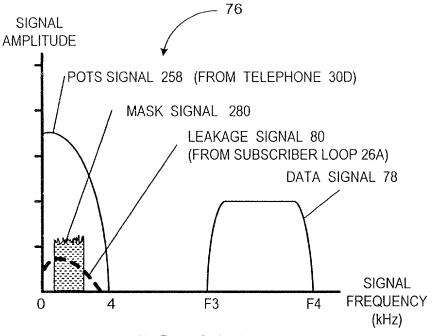


FIG. 16A

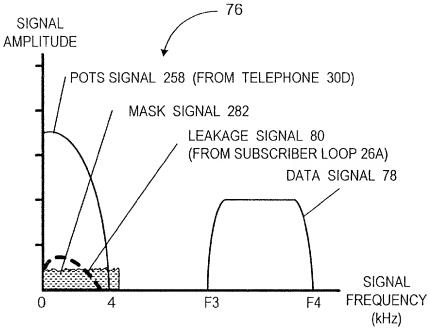


FIG. 16B

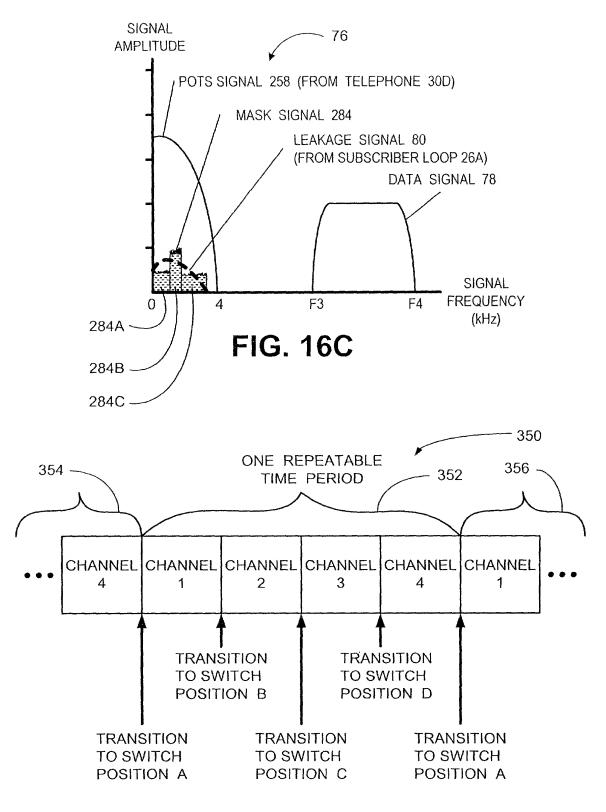


FIG. 18

